The use of Smart-IoT in different domains (Smart Manufacturing Systems, eHealth, smart-agriculture, etc.), imposes new challenges in design and verification of such embedded systems. These smart-components are often software dominated. The optimization and deployment of the software part become a complex task. In addition, and for performance purpose, the need to implement certain functions as hardware accelerators (IP: Intellectual Proprieties) is highly recommended. To choose the trade-off between the best mapping schemes a deep DSE (Design Space Exploration) is needed. To tackle this complexity, there is an urgent need to review co-design flow and related performance evaluation and optimisation techniques.

On the other hand, due to the heterogeneity of these components and their interdependencies, it is urgent to find a simple way for fast IoT-generation that guaranty interoperability and reconfiguration.

The aim of this session is to expose recent works on issues related to design, monitoring and verification of embedded systems used in IoT and smart-manufacturing, developing and using models for performance, security and safety purposes.

Topics of interest include, but not limited to:
- Co-Design.
- IoT.
- Performance.
- Verification and Validation.
- Industry 4.0.
- eHealth-IoT
- Security and Cyber-Security
- Reconfiguration
- Embedded OS.
- Embedded Systems.
- Smart Sensors.

Authors are invited to submit full-length (4 pages) papers, in IEEE format, using the guidelines in the authors’ info. Special session papers must be submitted by e-mail in PDF format to the organizers of the session. Accepted papers will be published in the electronic Conference Proceedings (CD ROM) and will be submitted to IEEE Xplore®.

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Important Dates:
- Research Paper Submission: October 10, 2018
- Notification of Acceptance: October 30, 2018
- Camera Ready Submission: November 10, 2018