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The 30th International Conference on Microelectronics















http://www.ieee-icm2018.org

16-19 December 2018, Tunisia



The International Conference on Microelectronics (ICM) has been held in numerous countries across the Middle East and Asia over the past 29 years. The 30th edition of the conference will be held in Tunisia.

ICM 2018 will include oral, poster sessions and tutorials given by experts in state-of-the-art topics. The regular technical program will run for three days. In addition, tutorial sessions will be held on the first day of the conference. Papers are solicited in, but not limited to, the following topics:

Eric Kerhervé, IMS, France Naim Ben Hamida, CIENA, Canada **Tutorials Co-Chairs**

Ahmed Madian, Nile Univ, Egypt

Hassen Mnif, ENETCom, Tunisia

Special Sessions Co-Chairs

Mohamed Atri, FSM, Tunisia Mohammad Baker, KH. Univ. AEU A. Al Maashari, SQ Univ. Oman

Panels Co-Chairs

Mohammed Ismail, WS. Univ. USA Amine Bermak, HBK Univ. Qatar Noureddine Boulejfen, CRMN, Tu.

Plenary Talk Co-Chairs

Magdy Bayoumi, Louisana Univ. USA Brahim Mezghani, ENIS, Tunisia Fabrice Monteiro, Lorraine Univ. Fr.

Industry Liaison Co-Chairs Mohamad Tabaa, EMSI, Morocco Ashraf Salem, M.G., Egypt

Zied Marrakchi, M.G., Tunisia

Publicity Co-Chairs

Piero Malcovati, Pavia Univ. Italy Abderrazak Jemai, INSAT, Tunisia Mohamed B. Salah, KACST, KSA

Publication Co-Chairs

Abdallah Kassem, NDU, Lebanon Mouna Baklouti, ENIS, Tunisia Mounir Samet, ENIS, Tunisia

Finance Co-Chairs

Kais Loukil, ESCS, Tunisia Tarek Ouni, ENETCom, Tunisia

Local Arrangement Chair Wassim Jmal, ISSATG, Tunisia

Local Arrangement Members Agnès Ghorbel, ENIS, Tunisia Amina Magdich, FSEGs, Tunisia Dorra Mallouli, ENIS, Tunisia Imen Ghorbel, ENETCom, Tunisia Manel Elleuchi, ENIS, Tunisia Rahma Aloulou, CRNS, Tunisia Tarek Frikha, ENIS, Tunisia Yassin Aydi, ENIS, Tunisia



Circuits and Systems

- 1. Analog and RF circuit design techniques
- 2. Digital signal and data processing
- 3. Wireless com. systems
- 4. Nonlinear circuits
- 5. System on Chip
- 6. VLSI for Signal and Image Processing
- 7. Integrated antenna and front-end co-design
- 8. Signal Processing in Communications.

CAD Tools and Design

- 1. Simulation (process, device, circuit, logic, timing, functional)
- 2. Layout (plac., rout., floor plan., symbolic, ERC, DRC)
- 3. Silicon optimization
- 4. Parallel embedded systems
- 5. Testing: Formal verification
- 6. CAD for FPGAs
- 7. High level synthesis tools
- 8. Design for testability.

Micro/Nanoelectronics

- 1. Device characterization and modelling
- 2. Device physics and novel structures
- 3. Process techno-logy, CMOS, BJT,
- 4. BiCMOS, GaAs
- 5. Reliability and failure analysis
- 6. Optoelectronics
- 7. MEMS Devices
- 8. Packaging and surface mount technology.

Perspective authors are invited to submit full-length (4 pages) papers, in IEEE format, using the guidelines in the authors' info. Only electronic submissions will be accepted via the online submission system. Accepted papers will be published in the electronic Conference Proceedings (CD ROM) and will be submitted to IEEE Xplore®. Authors of the best papers will be invited to resubmit an extended version to be considered for publication at an International Journal.

Deadlines:

• Full Paper Submission: August 10, August 20, 2018

• Notification of Acceptance: September 16, September 30, 2018

• Camera Ready Submission: October 11, 2018

For any information, contact:

General co-Chairs

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International Coordinators

Falah Awwad, UAEU, UAE Fouad El Haj Hassan, LU, Lebanom Khaled Salama, KAUST, KSA Mohamed Masmoudi, ENIS, Tunisia Said Belkouche, UCA, Morocco Montassar Maamoun, Blida, Algeria Abbes Amira, Qatar Univ, Qatar Abdoul Rjoub, JUST, Jordan Ahmed Ammari, SQ Univ. Oman Mohab Anis, AUC, Egypt Abbas Dandache, Lorraine U, France

















